



P.E.S INSTITUTE OF TECHNOLOGY - BANGALORE SOUTH CAMPUS
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME - TABLE FOR THE ACADEMIC YEAR JULY - DEC 2019

SEMESTER- 5A

ROOM - 603

Day	S1 8:00 - 9:00	S2 9:00 - 10:00		S3 10:30 - 11:30	S4 11:30 - 12:30		S5 1:15 - 2:15	S6 2:15 - 3:15	
Monday	ITC	DSP	B R E A K 10 : 00 to 10 : 30	MED	MED	B R E A K 12 : 30 to 01 : 15	C++	C++	
Tuesday	DSP-LI	HDL		HDL LAB-B1 DSP LAB-B2				DSP	MED
Wednesday	MSP430	C++		HDL-LI	HDL			LIB	ITC
Thursday	ITC	DSP		MSP430	MSP430			HDL LAB-B2 DSP LAB-B1	
Friday	HDL	HDL		DSP	ASD			ITC	LIB
Saturday									
SUBJECT		SUBJECT CODE	FACULTY		SUBJECT	SUBJECT CODE	FACULTY		
Management and Entrepreneurship		17ES51	Prof. Pattabhiraman		DSP LI		Prof. Bivas		
Digital Signal Processing		17EC52	Prof. Sireesha		HDL LI		Prof. Rajesh		
Verilog HDL		17EC53	Prof. Chiranjeevi		DSP LAB B1	17ECL57	Prof. Bivas, Prof. Rammurthy		
Information Theory and Coding		17EC54	Prof. Rajesh		DSP LAB B2		Prof. Sireesha, Prof. Bivas		
MSP430		17EC553	Prof. Chiranjeevi	Group 1	HDL LAB B1	17ECL58	Prof. Rajesh, Prof. Lavanya		
			Prof. Lavanya	Group 2					
Object Oriented Program using C++		17EC562	Prof. Pattabraman	Group 1	HDL LAB B2		Prof. Rajesh, Prof. Lavanya		
			Prof. Bivas	Group 2					



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SEMESTER- 5B

ROOM - 604

Day	S1 8:00 - 9:00	S2 9:00 - 10:00		S3 10:30 - 11:30	S4 11:30 - 12:30		S5 1:15 - 2:15	S6 2:15 - 3:15
Monday	HDL-LI	ITC	B R E A K 10 : 00 to 10 : 30	DSP	DSP	B R E A K 12 : 30 to 01 : 15	C++	C++
Tuesday	HDL LAB-B3 DSP LAB-B4			HDL	HDL		LIB	ITC
Wednesday	MSP430	C++		MED	ASD		ITC	DSP-LI
Thursday	MED	ITC		MSP430	MSP430		DSP	DSP
Friday	HDL LAB-B4 DSP LAB-B3			HDL	HDL		MED	LIB
Saturday								
SUBJECT		SUBJECT CODE	FACULTY		SUBJECT	SUBJECT CODE	FACULTY	
Management and Entrepreneurship		17ES51	Prof. Bivas Battacharya		DSP LI		Prof. Rammurthy	
Digital Signal Processing		17EC52	Prof. Ajey S.N.R		HDL LI		Prof. Raghavendra	
Verilog HDL		17EC53	Prof. Raghavendra		DSP LAB B3	17ECL57	Prof. Bivas, Prof. Pattabiraman	
Information Theory and Coding		17EC54	Prof. Rajesh		DSP LAB B4		Prof. Ramurthy, Prof. Pattabiraman	
MSP430		17EC553	Prof. Chiranjeevi	Group 1	HDL LAB B3	17ECL58	Prof. Raghavendra,	
			Prof. Lavanya	Group 2				
Object Oriented Program using C++		17EC562	Prof. Pattabiraman	Group 1	HDL LAB B4		Prof. Rajesh, Prof. Lavanya	
			Prof. Bivas	Group 2				



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SEMESTER- 5C

ROOM - 504

Day	S1 8:00 - 9:00	S2 9:00 - 10:00		S3 10:30 - 11:30	S4 11:30 - 12:30		S5 1:15 - 2:15	S6 2:15 - 3:15
Monday	MED	HDL-LI	B R E A K 10 : 00 to 10 : 30	HDL LAB-B5 DSP LAB-B6		B R E A K 12 : 30 to 01 : 15	C++	C++
Tuesday	DSP	ASD		ITC	ITC		HDL	MED
Wednesday	MSP430	C++		HDL LAB-B6 DSP LAB-B5			HDL	DSP
Thursday	HDL	HDL		MSP430	MSP430		DSP	LIB
Friday	DSP	DSP-LI		ITC	ITC		MED	LIB
Saturday								
SUBJECT		SUBJECT CODE	FACULTY		SUBJECT	SUBJECT CODE	FACULTY	
Management and Entrepreneurship		17ES51	Prof. Rammurthy		DSP LI		Prof. Sireesha	
Digital Signal Processing		17EC52	Prof. Sireesha		HDL LI		Prof. Raghavendra	
Verilog HDL		17EC53	Prof. Raghavendra		DSP LAB B5	17ECL57	Prof. Sireesha, Prof. Rammurthy	
Information Theory and Coding		17EC54	Prof. Ajey S.N.R		DSP LAB B6		Prof. Sireesha, Prof. Bivas	
MSP430		17EC553	Prof. Chiranjeevi	Group 1	HDL LAB B5	17ECL58	Prof. Raghavendra, Prof. Rajesh	
			Prof. Lavanya	Group 2				
Object Oriented Program using C++		17EC562	Prof. Bivas	Group 1	HDL LAB B6		Prof. Raghavendra, Prof. Lavanya	
			Prof. Pattabraman	Group 2				



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SEMESTER- 7A

ROOM - 600

Day	S1 8:00 - 9:00	S2 9:00 - 10:00		S3 10:30 - 11:30	S4 11:30 - 12:30		S5 1:15 - 2:15	S6 2:15 - 3:15
Monday	VLSI-LI	PE	B R E A K 10 : 00 to 10 : 30	ADC-LI	MMC	B R E A K 12 : 30 to 01 : 15	MAA	LIB
Tuesday	SC	SC		MAA	PE		VLSI LAB B2 ADC LAB B1	
Wednesday	MAA	ASD		Project Work	Project Work		DIP	DIP
Thursday	VLSI LAB B1 ADC LAB B2			Project Work	Project Work		DIP	DIP
Friday	MMC	MMC		SC	MAA		PE	PE
Saturday								

SUBJECT	SUBJECT CODE	FACULTY		SUBJECT	SUBJECT CODE	FACULTY
Microwave & Antenna	15EC71	Dr. Ananya		ADC LI		Dr. Ananya
Digital Image Processing	15EC72	Dr. Subhash Kulkarni		VLSI LI		Prof. Chiranjeevi
Power Electronics	15EC73	Prof. Lokesh		ADC LAB B1	15ECL76	Dr. Ananya, Prof. Renuka
Multimedia Communication	15EC741	Prof. Rammurthy	Group 1	ADC LAB B2		15ECL76
		Prof. Renuka	Group 2			
Satellite Communication	15EC755	Prof. Sanket N S	Group 1	VLSI LAB B1	15ECL77	Prof. Sanket N S, Prof. Chiranjeevi
		Prof. Lavanya	Group 2			
Project Work	15ECP78			VLSI LAB B2		Prof. Chiranjeevi, Prof. Lavanya



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SEMESTER- 7B

ROOM - 601

Day	S1 8:00 - 9:00	S2 9:00 - 10:00		S3 10:30 - 11:30	S4 11:30 - 12:30		S5 1:15 - 2:15	S6 2:15 - 3:15
Monday	PE	MAA	B R E A K 10 : 00 to 10 : 30	LIB	MMC	B R E A K 12 : 30 to 01 : 15	DIP	DIP
Tuesday	SC	SC		DIP	DIP		PE	PE
Wednesday	PE	MAA		Project Work	Project Work		VLSI LAB B3 ADC LAB B4	
Thursday	MAA	ASD		Project Work	Project Work		MAA	VLSI-LI
Friday	MMC	MMC		SC	ADC-LI		VLSI LAB B4 ADC LAB B3	
Saturday								
SUBJECT		SUBJECT CODE	FACULTY		SUBJECT	SUBJECT CODE	FACULTY	
Microwave & Antenna		15EC71	Dr. Ananya		ADC LI		Prof. Renuka	
Digital Image Processing		15EC72	Dr. Subhash Kulkarni		VLSI LI		Prof. Sanket N S	
Power Electronics		15EC73	Prof. Sanketh K		ADC LAB B3	15ECL76	Dr.Ananya, Prof. Renuka	
Multimedia Communication		15EC741	Prof. Rammurthy	Group 1	ADC LAB B4		Dr.Ananya, Prof. Lokesh	
			Prof. Renuka	Group 2				
Satellite Communication		15EC755	Prof. Sanket N S	Group 1	VLSI LAB B3	15ECL77	Prof. Sanket N S, Prof. Chiranjeevi	
			Prof. Lavanya	Group 2	VLSI LAB B4		Prof. Chiranjeevi, Prof. Sanketh	
Project Work		15ECP78						



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SEMESTER- 7C

ROOM - 602

Day	S1 8:00 - 9:00	S2 9:00 - 10:00		S3 10:30 - 11:30	S4 11:30 - 12:30		S5 1:15 - 2:15	S6 2:15 - 3:15
Monday	PE	LIB	B R E A K 10 : 00 to 10 : 30	VLSI-LI	MMC	B R E A K 12 : 30 to 01 : 15	VLSI LAB B5 ADC LAB B6	
Tuesday	SC	SC		MAA	DIP		PE	DIP
Wednesday	VLSI LAB B6 ADC LAB B5			Project Work	Project Work		MAA	MAA
Thursday	MAA	DIP		Project Work	Project Work		PE	PE
Friday	MMC	MMC		SC	ADC-LI		DIP	ASD
Saturday								

SUBJECT	SUBJECT CODE	FACULTY		SUBJECT	SUBJECT CODE	FACULTY
Microwave & Antenna	15EC71	Prof. Renuka Kajur		ADC LI		Prof. Lokesh
Digital Image Processing	15EC72	Dr. Shikha Tripaty		VLSI LI		Prof. Sanket N S
Power Electronics	15EC73	Prof. Lokesh		ADC LAB B5	15ECL76	Prof. Lokesh, Prof. Renuka
Multimedia Communication	15EC741	Prof. Rammurthy	Group 1	ADC LAB B6		Prof. Lokesh, Prof. Renuka
Satellite Communication	15EC755	Prof. Sanketh K	Group 1	VLSI LAB B5	15ECL77	Prof. Chiranjeevi, Prof. Sanket N S
		Prof. Lavanya	Group 2			
Project Work	15ECP78			VLSI LAB B6		Prof. Raghavendra